

ABSTRACT OF THE DISCLOSURE

An improved method is provided for seamlessly combining adjacent subchannels in a signal processing system. Each adjacent subchannel is defined to include a first and a second linear phase digital filter, where the filters are implemented using digital circuitry having a plurality of registers. Prior to summing the two adjacent channels, a delay is introduced into one of the two filters in each of the subchannels, such that the delay is equal to the delay associated with one register in the digital circuitry. The two adjacent subchannels are then combined to form a composite channel that exhibits linear phase.